

Howrey Docket No. 01827.0037.00

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Donald B. Eidson, et al.

Application No.: 09/602,690

Filed: June 23, 2000

RATE N/N SYSTEMATIC, For:

> RECURSIVE CONVOLUTIONAL **ENCODER AND CORRESPONDING**

DECODER

Art Unit: 2734

Examiner: Unassigned

RECEIVED

DEC 0 1 2003

**Technology Center 2100** 

TRANSMITTAL OF SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT TO 23 2004 TO UNDER 37 C.F.R. §1.56, 1.97 and 1.98

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313

Sir:

Applicants submit the following documents for appropriate action by the U.S. Patent a

## Trademark Office:

- $\boxtimes$ Supplemental Information Disclosure Statement under 37 C.F.R. §1.56, 1.97 and
- Form PTO-1449 (1 page) with 3 references;  $\times$
- X Return postcard.

CERTIFICATE OF MAILING (37 C.F.R. §1.8a)

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

November 2003

Date of Deposit

Robin L. Clow

Name of Person Mailing Paper

Signature of Person Mailing Paper

Applicant believes no fees are due in connection with this Petition. However, if any fees are in fact due, the Commissioner is hereby authorized to be charged to our Deposit Account No.08-3038 referencing Docket No. 01827.0037.00US00.

Respectfully submitted,

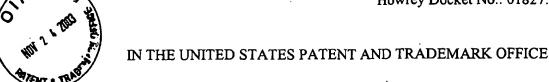
Dated: November 19, 2003.

Robert C. Laurenson, Reg. No. 34,206

HOWREY SIMON ARNOLD & WHITE, LLP 301 Ravenswood Avenue, Box No. 34 Menlo Park, CA 94025

Facsimile: (650) 463-8400 Telephone: (949) 759-5269

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

Applicant hereby submits, without admission of prior art effect thereof, the documents listed on the accompanying Form PTO-1449 pursuant to the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98.

Applicant has listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

| CERTIFICATE OF MAILING |
|------------------------|
| (37 C.F.R. §1.8a)      |

I hereby certify that this paper (along with any referred to as being attached hereto) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Howrey Docket No.: 01827.0037.00US00

Each item of information contained in the Information Disclosure Statement submitted herewith was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Therefore, no fee is required.

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and to indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 08-3038.

Respectfully submitted,

Date: November \_\_\_\_\_\_\_, 2003

Robert C. Laurenson

Reg. No. 34,206

HOWREY SIMON ARNOLD & WHITE, LLP 301 Ravenswood Avenue, Box No. 34 Menlo Park, CA 94025 FAX No. (650) 463-8400 Telephone No. (949) 759-5269

|                     |   |                                       | · .         |   |       | •                        |                |
|---------------------|---|---------------------------------------|-------------|---|-------|--------------------------|----------------|
| FORM PTO-1449       |   |                                       |             | ATTY. DOCKET NO.<br>01827.0037.00US00   |       | SERIAL NO.<br>09/602,690 |                |
|                     | TENTS AND OTHER IT<br>ORMATION DISCLOSU |                                       |             | APPLICANT:<br>Donald B. Eids  |       | 0)/002,03                | <u> </u>       |
| MONT                | Use several sheets if                   | necessary)                            | )<br>       | FILING DATE:<br>June 23, 2000   |       | <b>GROUP:</b> 2734       |                |
| TA TRABENIA         |   | U.S. P                                | ATENT DOC   | UMENTS  |       |                          |                |
| EXAMINER<br>INITIAL | DOCUMENT NUMBER                         | DATE                                  |             | NAME  | CLASS | SUB<br>CLASS             | FILING<br>DATE |
|                     |   | · · · · · · · · · · · · · · · · · · · | None        |   | - RI  | CEI                      | ED             |
|                     |   | <u></u>                               | <u> </u>    |   |       |                          | 2003           |
|                     |   | FOREIGN                               | N PATENT DO | CUMENTS   | Tech  | inology Cer              | nter 2100      |
| EXAMINER            | DOCUMENT NUMBER                         | DATE                                  |             | NAME OF THE PARTY |       | SUB                      | TRANSLATION    |

|      | OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)   |
|------|--|
| Al   | Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals, Jet Propulsion Laboratory, October 25-29, 1999.         |
| A2   | Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART I, Jet Propulsion Laboratory, October 25-29, 1999.  |
| A3 · | Benedetto, S. and Divsalar, D., "Turbo Codes: Analysis, Design, Iterative Decoding and Applications," Course 909, Int'l Courses for Telecom and Semiconductor Professionals PART II, Jet Propulsion Laboratory, October 25-29, 1999. |

None

| EXAMINER:   | DATE CONSIDERED: |  |  |  |  |
|---|------------------|--|--|--|--|
| EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Drawline through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant |                  |  |  |  |  |